

Notice of References Cited	Application/Control No. 09/803,715	Applicant(s)/Patent Under Reexamination LI, JIA	
	Examiner Julio J. Maldonado	Art Unit 2823	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-4,110,899	09-1978	Nagasawa et al.	438/227
*	B	US-5,169,792	12-1992	Kato et al.	257/398
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
*	N	JP 63-271956	09-1998	JP	Hosaka	H 01L 21/76
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Silicon Processing for the VLSI Era, Volume 2, 1990 by Lattice Press, pages 38-41
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.